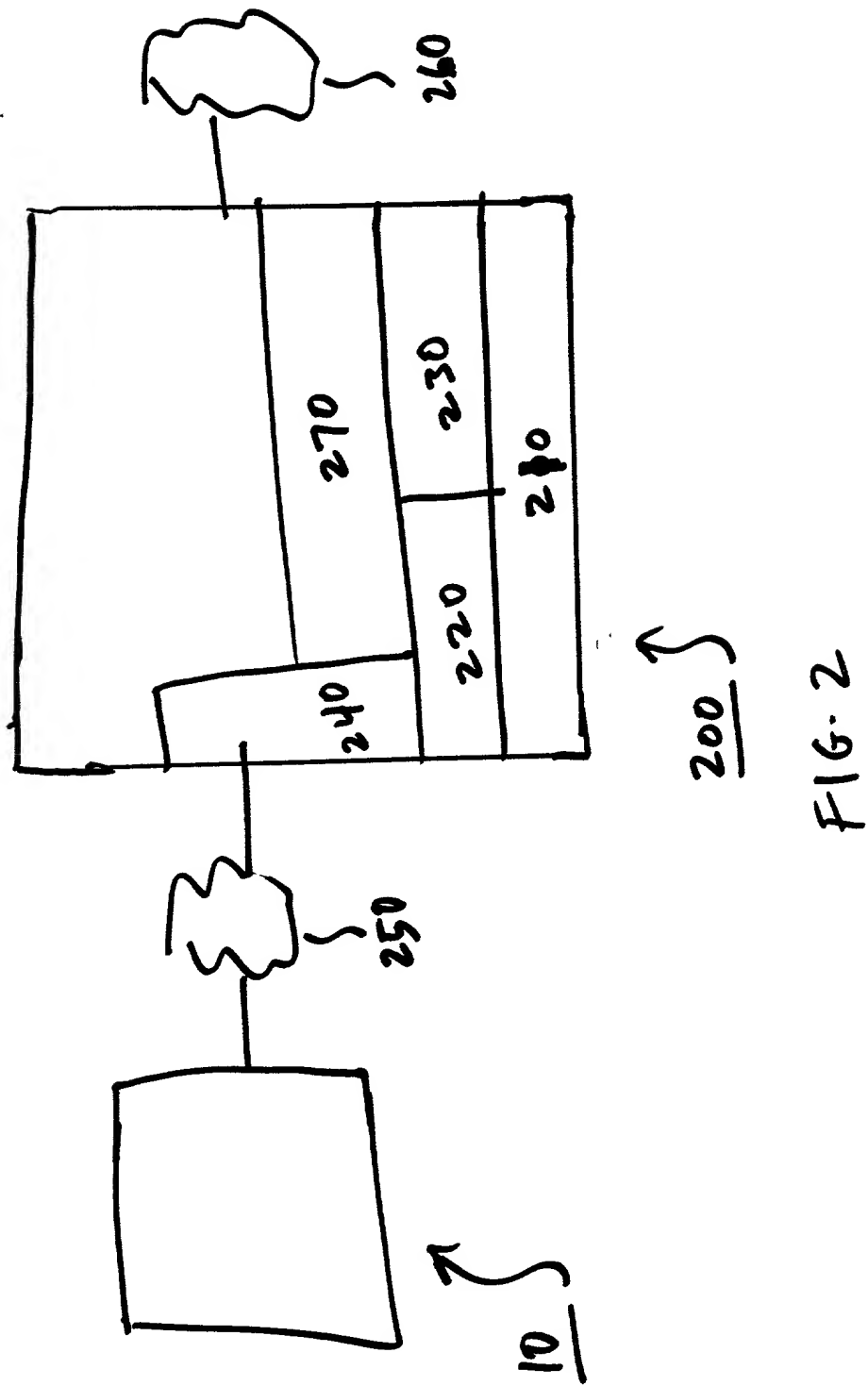
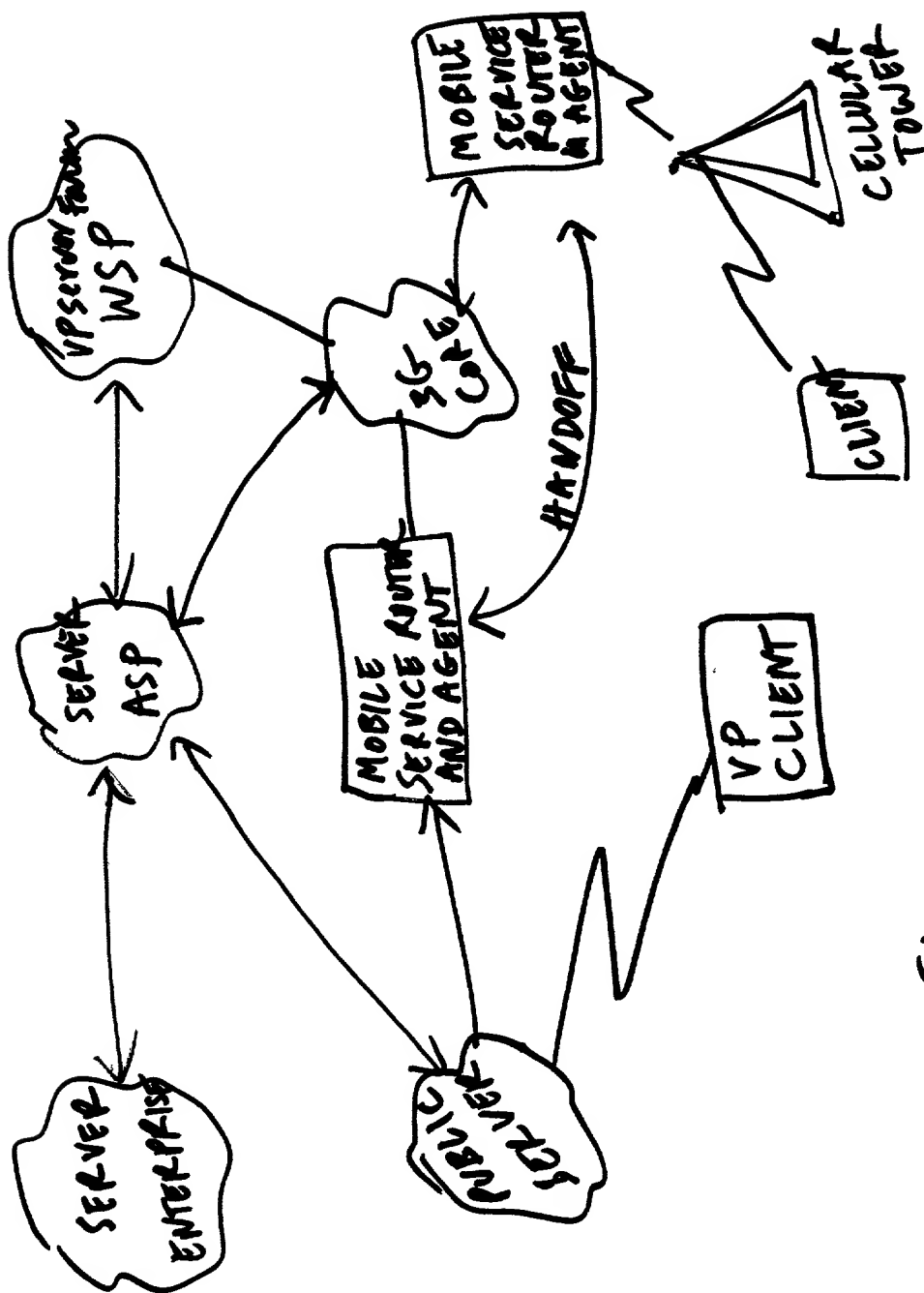


FIG. 1

10

FIG. 1 is a block diagram of a system architecture. The system includes a central processing unit (CPU) 10, a memory unit 20, a storage unit 30, a network interface unit 40, a user interface unit 50, and a power supply unit 60. The CPU 10 is connected to the memory unit 20, the storage unit 30, the network interface unit 40, and the user interface unit 50. The power supply unit 60 is connected to the CPU 10 and the memory unit 20. The network interface unit 40 is connected to the storage unit 30 and the user interface unit 50. The user interface unit 50 is connected to the power supply unit 60.





FLG 3

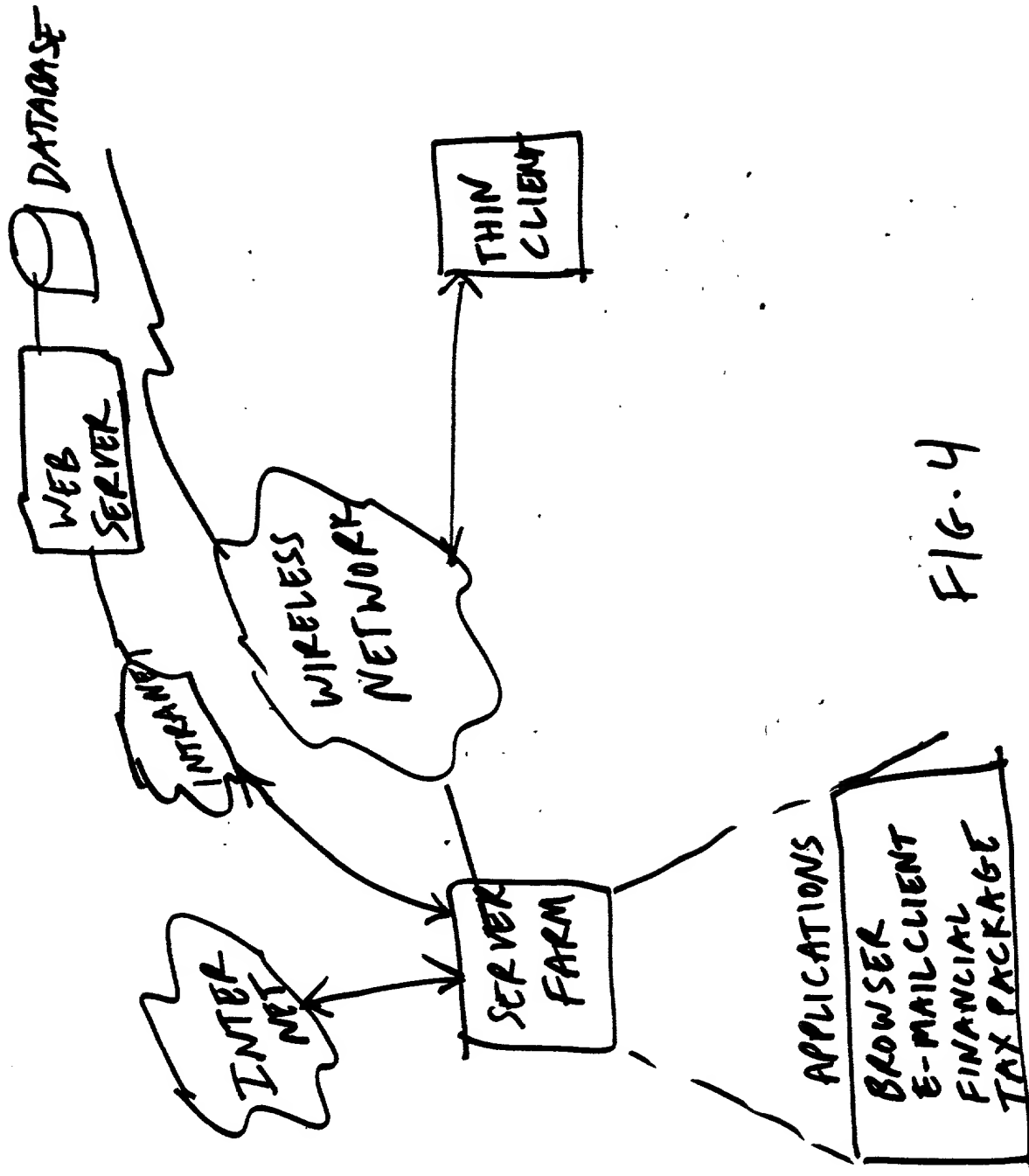


FIG. 4

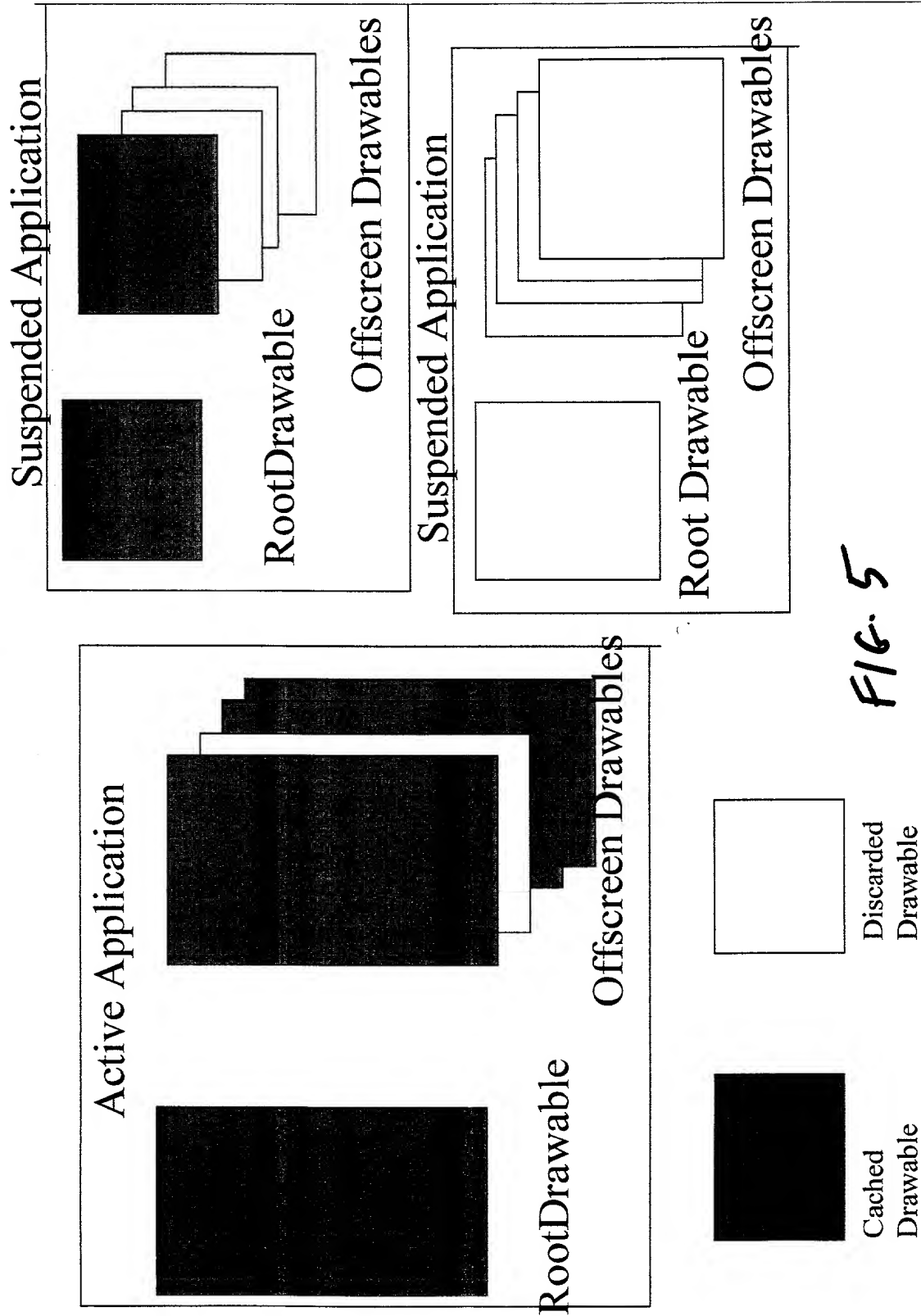
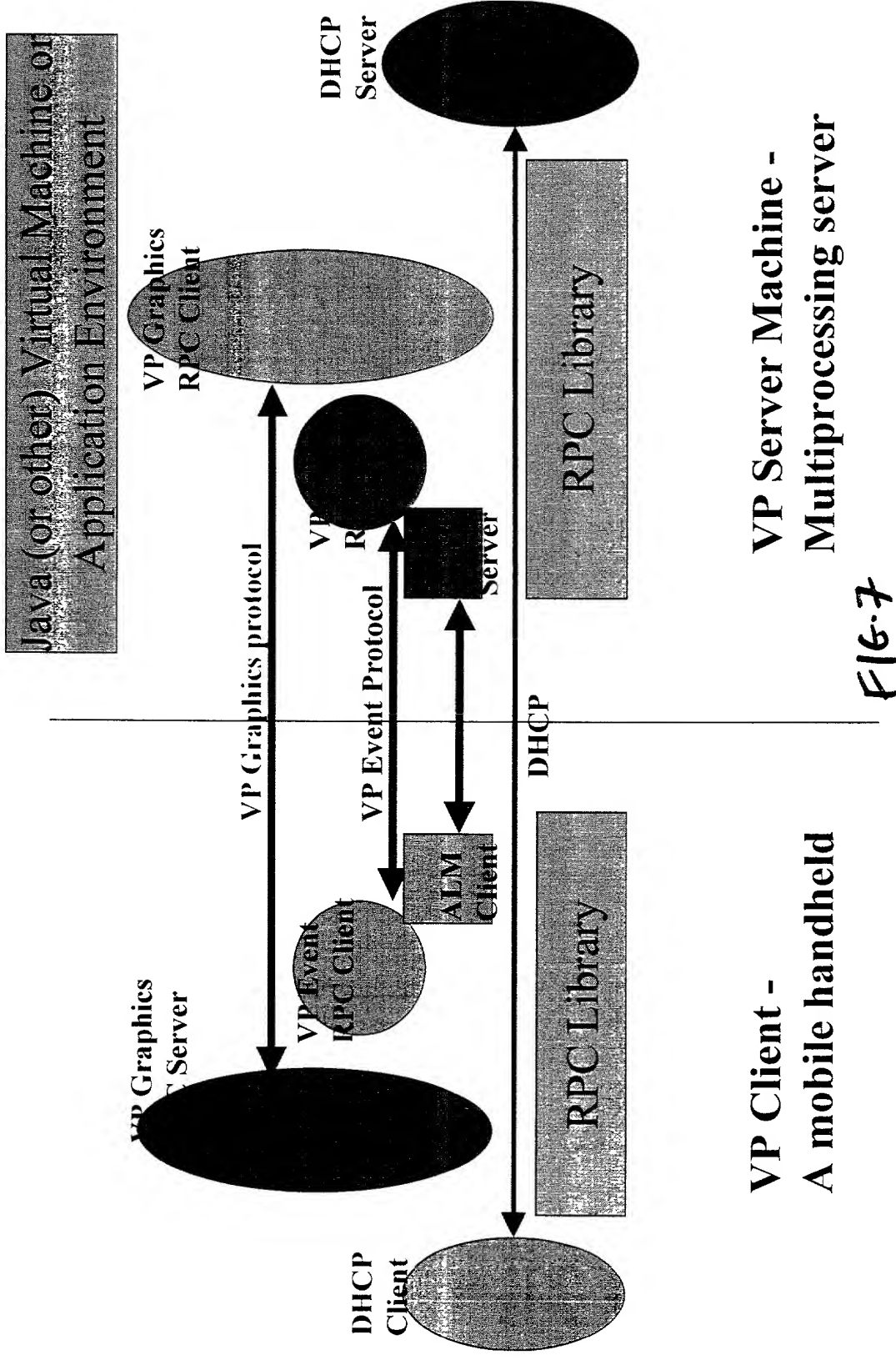


Fig. 5



FIG. 6



F16-7

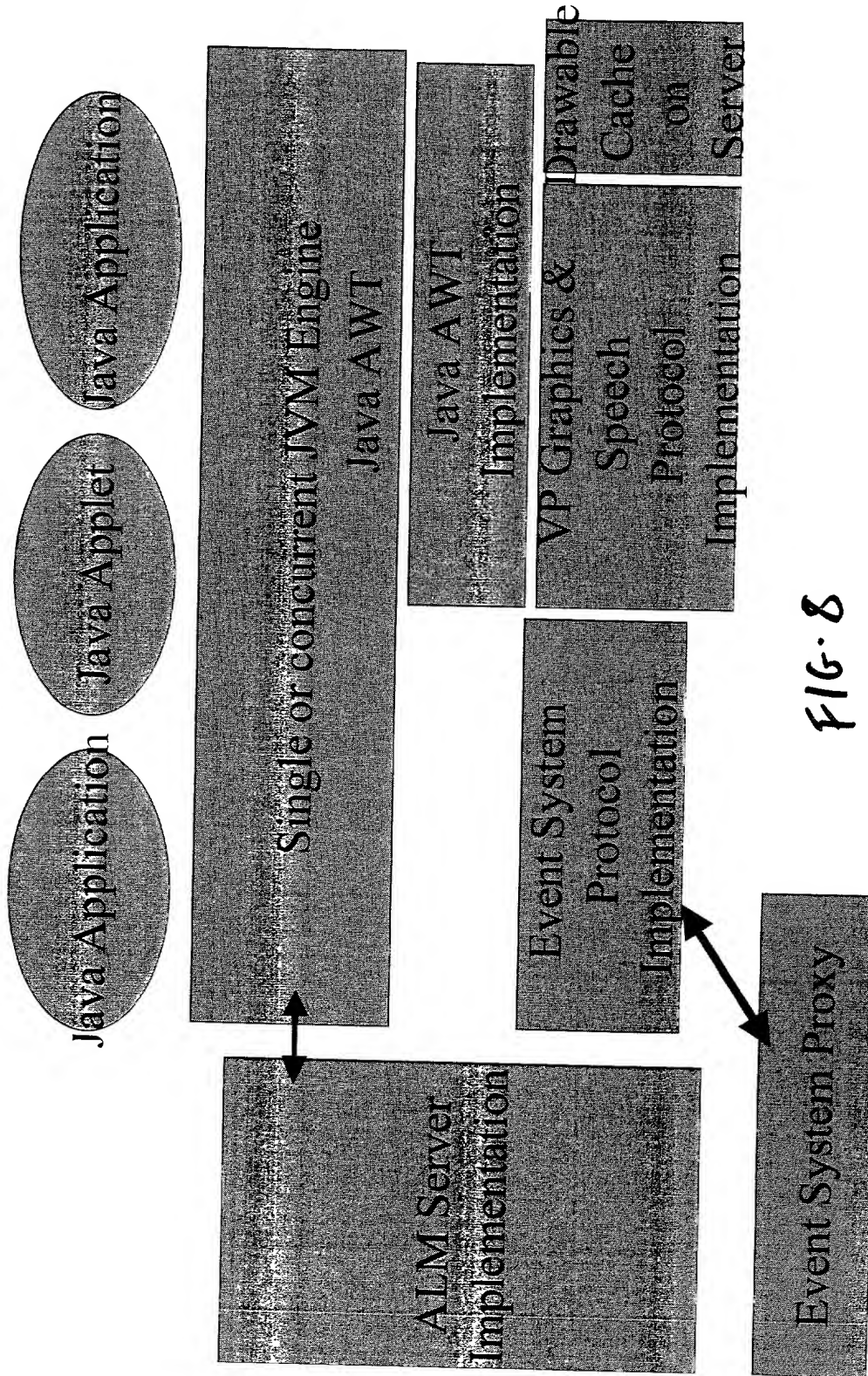
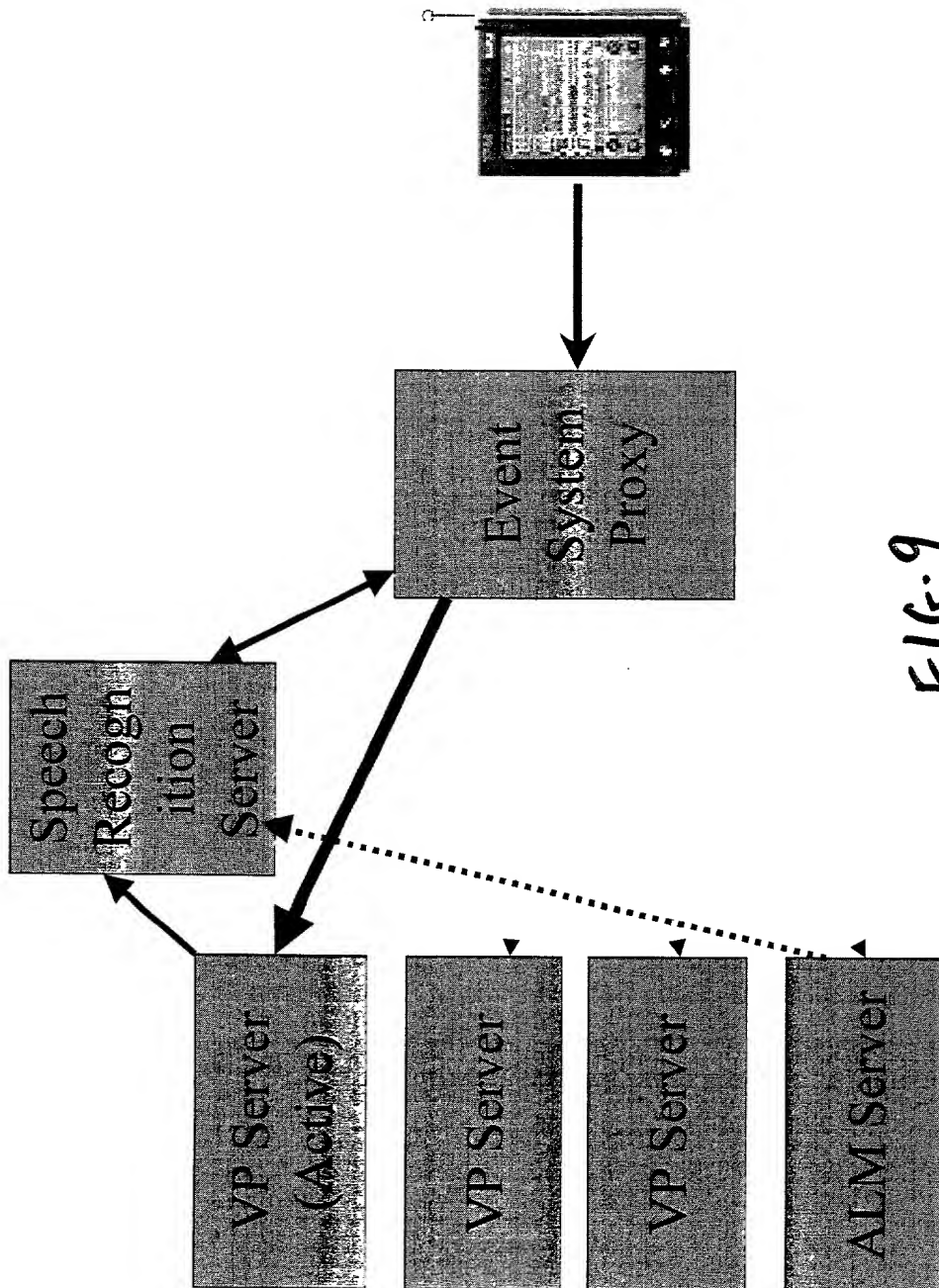


FIG. 8

A server farm configuration with Voice



F16-9

Event System Interaction for a multimodal application

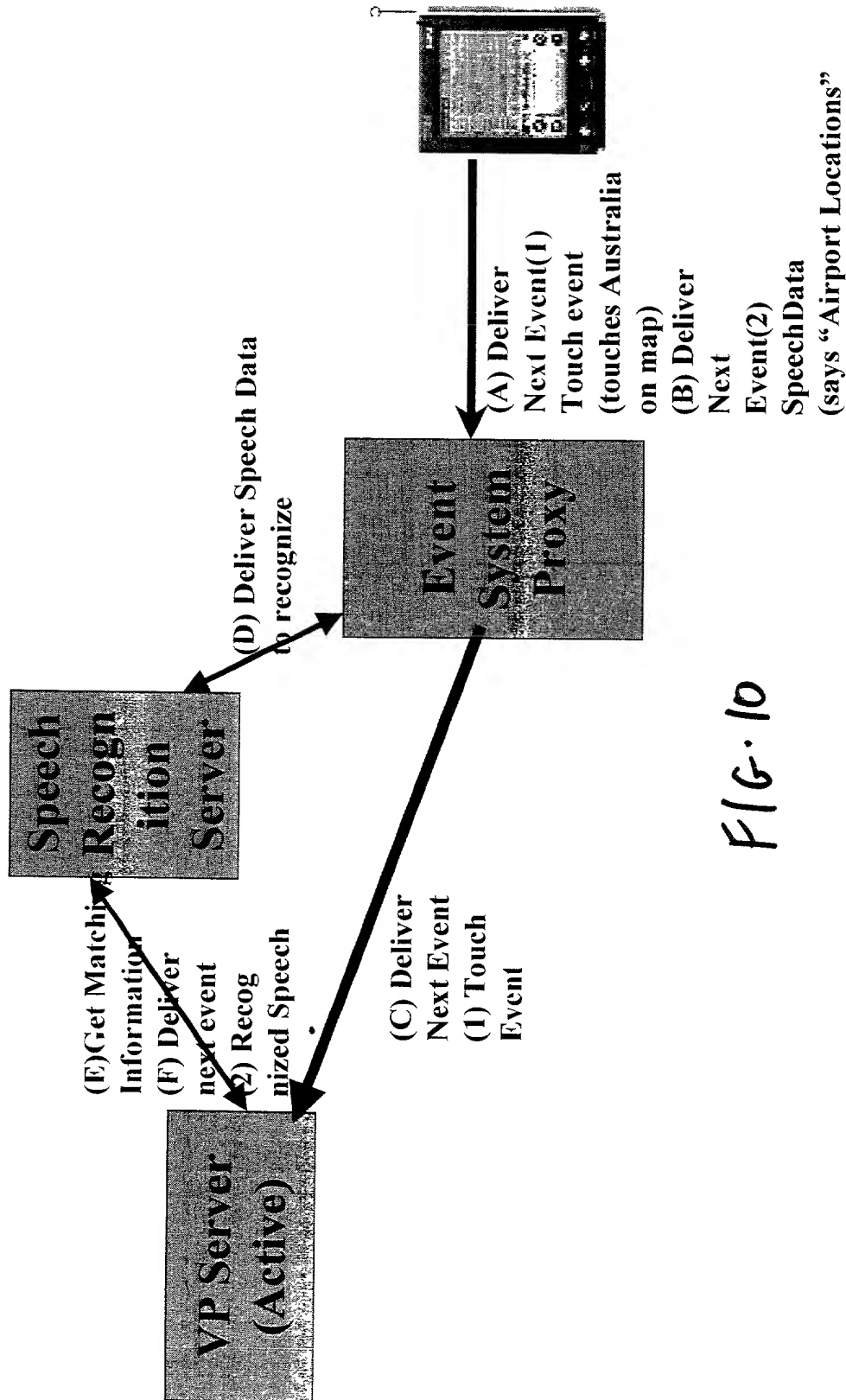


FIG. 10